

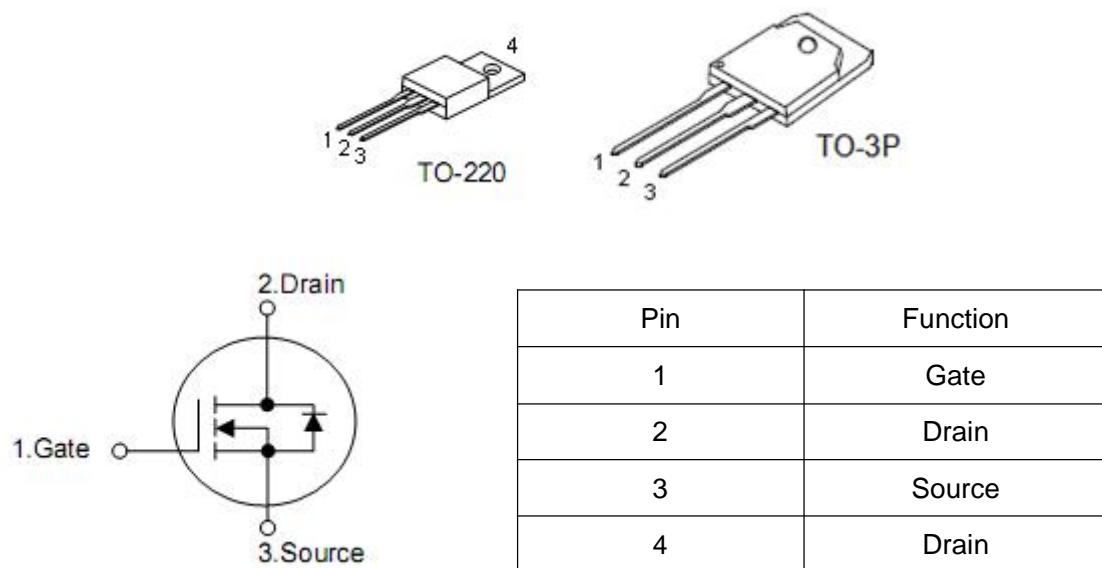
1. Features

- $R_{DS(ON)}=0.08\Omega$ @ $V_{GS}=10V$
- RoHS compliant
- Low on resistance
- Low gate charge
- Fast switching

2. Applications

- DC-DC converters
- DC-AC converters for UPS
- SMPS and motor controls

3. Symbol



4. Absolute maximum ratings

($T_C=25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Rating	Units
Drain-source voltage	V_{DSS}	200	V
Continuous drain current	I_D	40	A
Continuous drain current $T_C=100^\circ\text{C}$		19.2	A
Pulsed drain current, $V_{GS}=10\text{V}$ (note*1)	I_{DM}	120	A
Power dissipation	P_D	175	W
Derating factor above 25°C		1.43	W/ $^\circ\text{C}$
Gate-source voltage	V_{GS}	± 30	V
Single pulse avalanche energy (note*2)	E_{AS}	800	mJ
Avalanche current (note*1)	I_{AR}	32	A
Repetitive avalanche energy (note*1)	E_{AR}	17.5	mJ
Peak diode recovery dv/dt (note*3)	dv/dt	4.5	V/ns
Operating junction and storage temperature range	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$
Maximum temperature for soldering 1/8" from case for 5 seconds	T_L	300	$^\circ\text{C}$

5. Thermal characteristics

Parameter	Symbol	TO-220	TO-3P	Unit
Junction-case	$R_{\theta JC}$	0.49	0.6	$^\circ\text{C}/\text{W}$
Case-sink typ	$R_{\theta JS}$	0.5	-	
Junction-ambient	$R_{\theta JA}$	62.5	60	$^\circ\text{C}/\text{W}$

6. Electrical characteristics

($T_J=25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Drain-source breakdown voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	200	-	-	V
Breakdown voltage temperature coefficient Figure 11	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Reference 25°C $I_{\text{D}}=250\mu\text{A}$	-	0.2	-	$\text{V}/^\circ\text{C}$
Drain-source leakage current	I_{DSS}	$V_{\text{DS}}=200\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
		$V_{\text{DS}}=160\text{V}, T_J=125^\circ\text{C}$	-	-	10	
Gate-source forward leakage	I_{GSS}	$V_{\text{GS}}=30\text{V}$	-	-	100	nA
Gate-source reverse leakage		$V_{\text{GS}}=-30\text{V}$	-	-	-100	
Drain-source on-resistance Figure 9 and 10	$R_{\text{DS(on)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=16\text{A}$	-	0.08	0.1	Ω
Gate threshold voltage, Figure 12	$V_{\text{GS(TH)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2	-	4	V
Forward transconductance	g_{fs}	$V_{\text{DS}}=40\text{V}, I_{\text{D}}=16\text{A}$ (note*4)	-	22	-	S
Input capacitance	C_{iss}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}$ $f=1\text{MHz}$ Figure 14	-	1560	-	pF
Output capacitance	C_{oss}		-	370	-	
Reverse transfer capacitance	C_{rss}		-	150	-	
Turn-on delay time	$t_{\text{d(on)}}$	$V_{\text{DD}}=100\text{V}, I_{\text{D}}=32\text{A}, R_{\text{G}}=25\Omega, V_{\text{GS}}=10\text{V}$	-	26	-	ns
Rise time	t_r		-	32	-	
Turn-off delay time	$t_{\text{d(off)}}$		-	141	-	
Fall time	t_f		-	83	-	
Total gate charge	Q_g	$V_{\text{DS}}=160\text{V}, I_{\text{D}}=32\text{A}, V_{\text{GS}}=10\text{V}$	-	50	-	nC
Gate-source charge	Q_{gs}		-	12	-	
Gate-drain ("Miller")charge	Q_{gd}		-	22	-	
Continuous source current (body diode)	I_s	Integral pn-diode in MOSFET	-	-	40	A
Maximum pulsed current (body diode)	I_{SM}		-	-	128	
Diode forward voltage	V_{SD}	$I_{\text{S}}=32\text{A}, V_{\text{GS}}=0\text{V}$	-	-	1.4	V
Reverse recovery time	t_{rr}	$I_{\text{S}}=32\text{A}, V_{\text{GS}}=0\text{V}$ $dI/dt=100\text{A}/\mu\text{s}$	-	215	-	ns
Reverse recovery charge	Q_{rr}		-	1.8	-	uC

Note: *1. $I_{\text{AS}}=32\text{A}, V_{\text{DD}}=50\text{V}, R_{\text{G}}=25\Omega, T_J=25^\circ\text{C}$

*2. Repetitive rating; pulse width limited by maximum junction temperature.

*3. $I_{\text{SD}} \leq 40\text{A}$ $dI/dt \leq 200\text{A}/\mu\text{s}$, $V_{\text{DD}} \leq \text{BV}_{\text{DSS}}$, $T_J=175^\circ\text{C}$.

*4. Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

*5. Essentially independent of operating temperature.

7. Typical operating characteristics

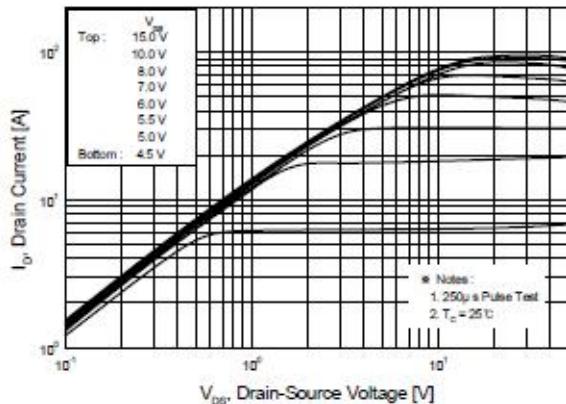


Figure 1. On-Region Characteristics

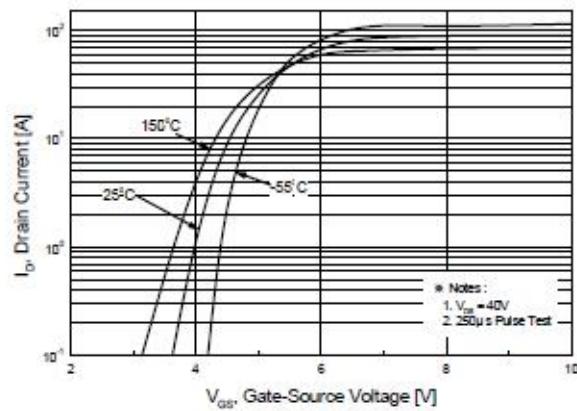


Figure 2. Transfer Characteristics

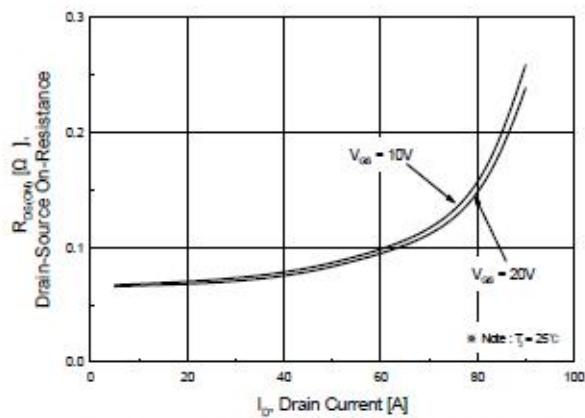


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

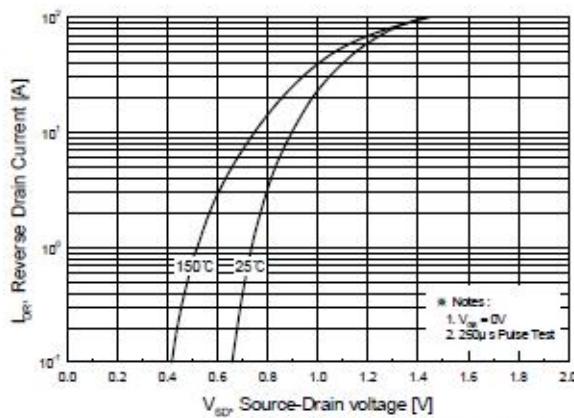


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

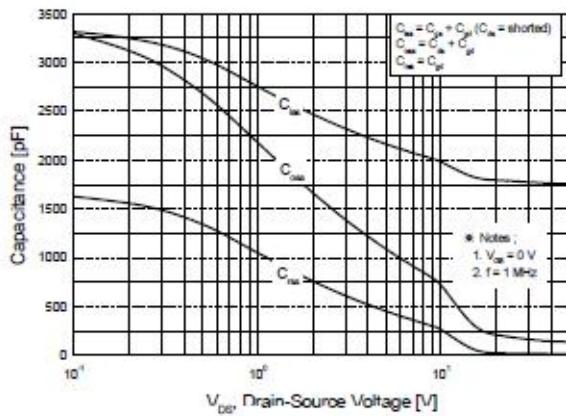


Figure 5. Capacitance Characteristics

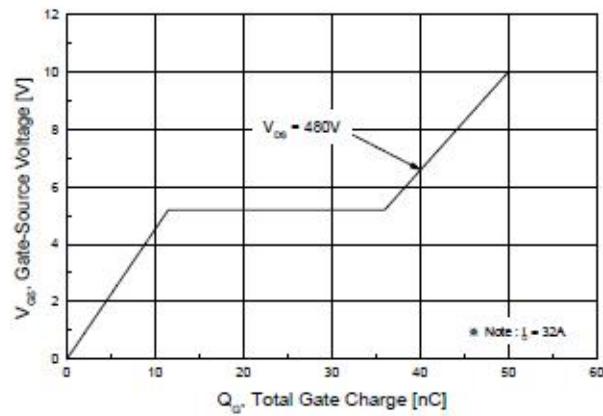
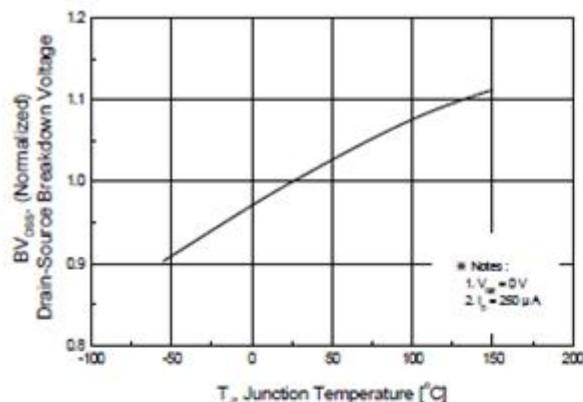
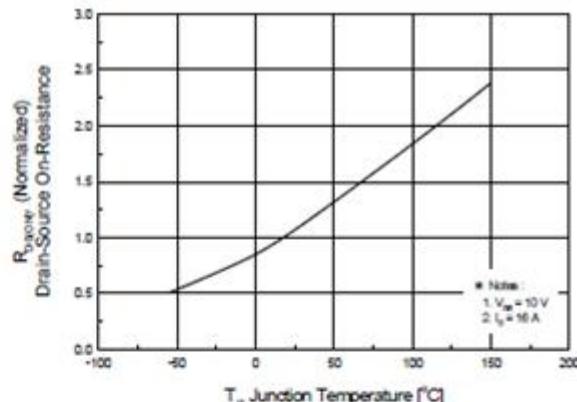


Figure 6. Gate Charge Characteristics



**Figure 7. Breakdown Voltage Variation
vs Temperature**



**Figure 8. On-Resistance Variation
vs Temperature**

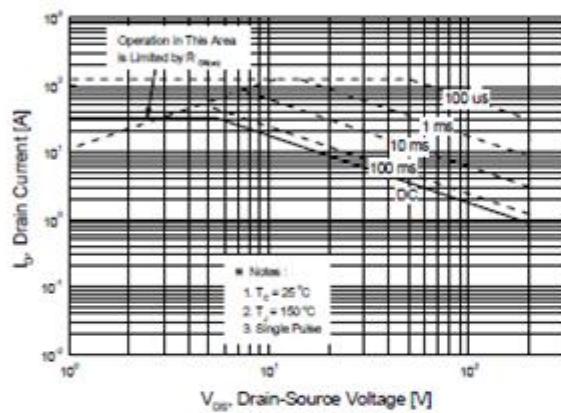
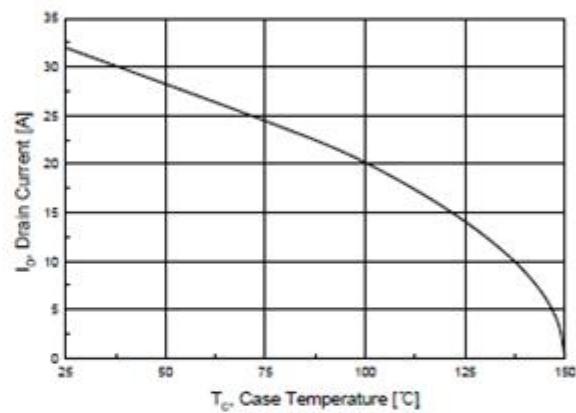


Figure 9. Maximum Safe Operating Area



**Figure 10. Maximum Drain Current
vs Case Temperature**

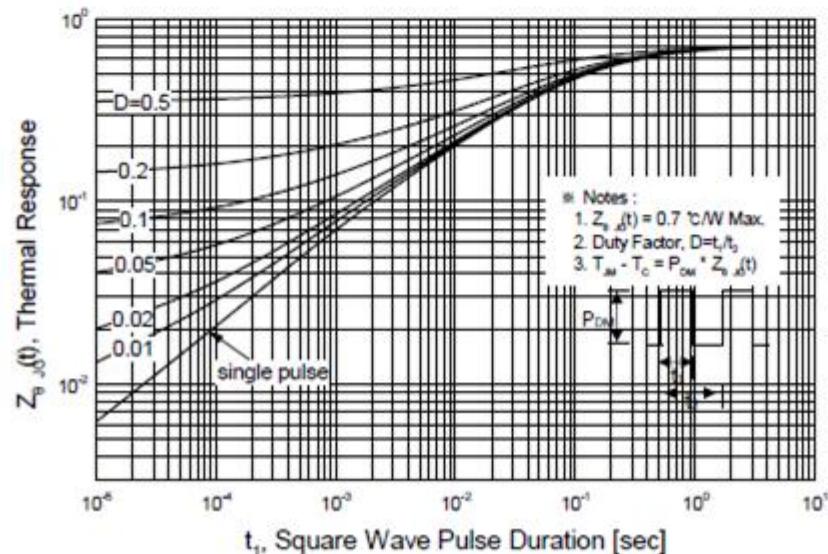


Figure 11. Transient Thermal Response Curve