

General Description

The 70P10 is the highest performance trench P-Ch MOSFET with extreme high cell density, which provide excellent $R_{DS(on)}$ and gate charge for most of the small power switching and load switch applications.

The 70P10 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

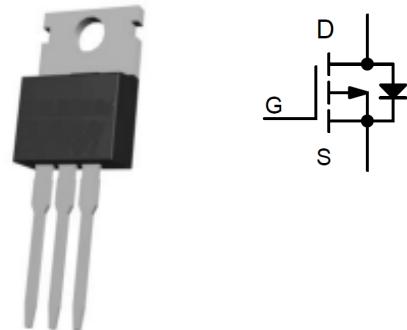
Product Summary

BV_{DSS}	R_{DS(on)}	I_D
-100V	18mΩ	-70A

Applications

- Inverters

TO-220AB Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
Common Ratings ($T_C=25^\circ\text{C}$ Unless Otherwise Noted)			
V_{DSS}	Drain-Source Voltage	-100	V
V_{GSS}	Gate-Source Voltage	± 25	
T_J	Maximum Junction Temperature	175	°C
T_{STG}	Storage Temperature Range	-55 to 175	°C
I_S	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	-70
			A
Mounted on Large Heat Sink			
$I_{DP}^{(1)}$	300μs Pulse Drain Current Tested	$T_C=25^\circ\text{C}$	-240
$I_D^{(2)}$	Continuous Drain Current($V_{GS}=-10\text{V}$)	$T_C=25^\circ\text{C}$	-70
		$T_C=100^\circ\text{C}$	-45
P_D	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	190
		$T_C=100^\circ\text{C}$	95
$R_{\theta JC}$	Thermal Resistance-Junction to Case	0.8	°C/W
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62.5	°C/W
Drain-Source Avalanche Ratings			
$E_{AS}^{(3)}$	Avalanche Energy, Single Pulsed	400	mJ

Electrical Characteristics (T_C=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =-250μA	-100			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-100V, V _{GS} =0V			-1	μA
		T _J =125°C			-30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =-250μA	-2		-4	V
I _{GSS}	Gate Leakage Current	V _{GS} =±25V, V _{DS} =0V			±100	nA
R _{DS(ON)} ^④	Drain-Source On-state Resistance	V _{GS} =-10V, I _{DS} =-60A		18	25	mΩ
Diode Characteristics						
V _{SD} ^④	Diode Forward Voltage	I _{SD} =-30A, V _{GS} =0V			-1.5	V
t _{rr}	Reverse Recovery Time	I _{SD} =-60A, dI _{SD} /dt=100A/μs		175		ns
Q _{rr}	Reverse Recovery Charge			620		nC
Dynamic Characteristics ^⑤						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz		2		Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-50V, Frequency=1.0MHz		4200		pF
C _{oss}	Output Capacitance			615		
C _{rss}	Reverse Transfer Capacitance			380		
t _{d(ON)}	Turn-on Delay Time	V _{DD} =-50V, I _{DS} =-60A, V _{GEN} =-10V, R _G =6Ω		27		ns
t _r	Turn-on Rise Time			83		
t _{d(OFF)}	Turn-off Delay Time			145		
t _f	Turn-off Fall Time			40		
Gate Charge Characteristics ^⑤						
Q _g	Total Gate Charge	V _{DS} =-80V, V _{GS} =-10V, I _{DS} =-60A		164		nC
Q _{gs}	Gate-Source Charge			34		
Q _{gd}	Gate-Drain Charge			50		

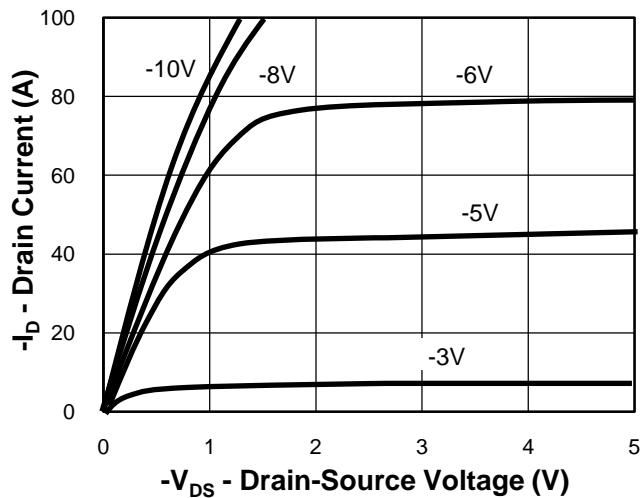
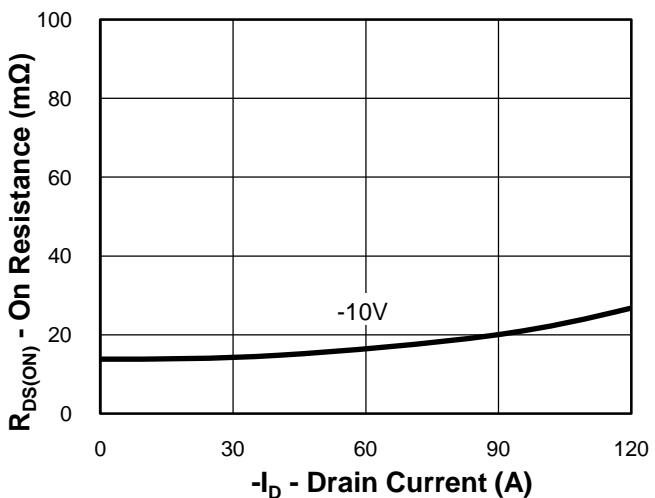
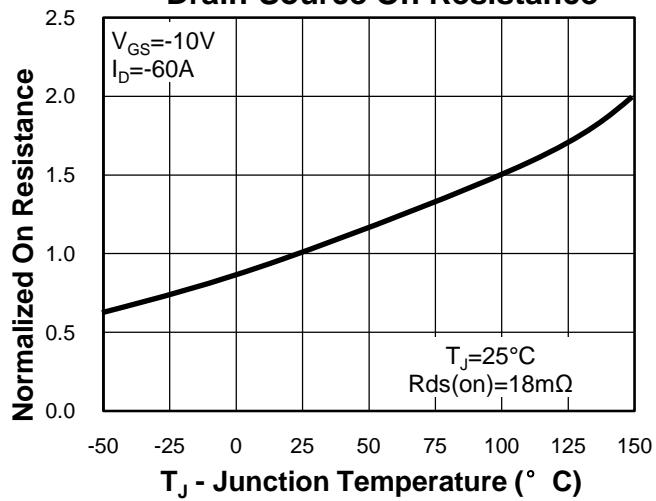
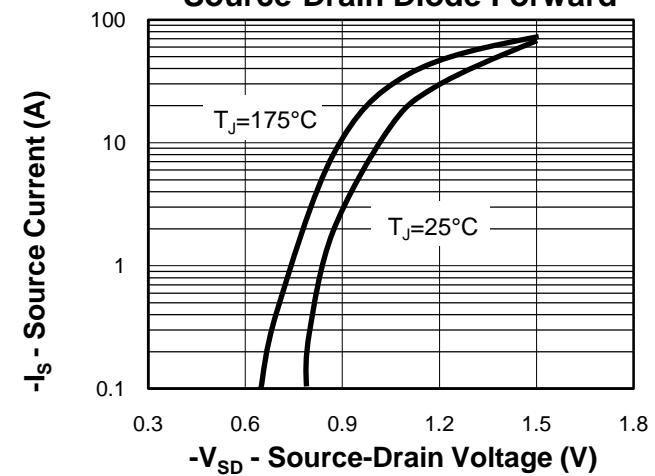
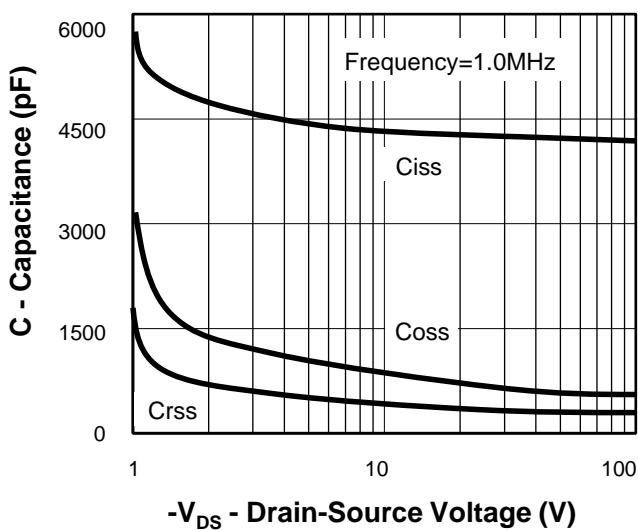
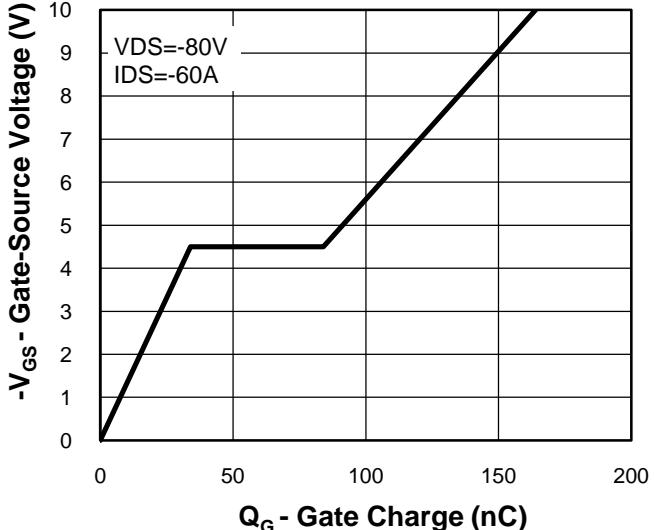
Notes: ①Pulse width limited by safe operating area.

②Calculated continuous current based on maximum allowable junction temperature.

③Limited by T_{Jmax}, I_{AS}=-40A, V_{DD}=-60V, R_G=50Ω, Starting T_J=25°C.

④Pulse test; Pulse width ≤ 300μs, duty cycle ≤ 2%.

⑤Guaranteed by design, not subject to production testing.

Typical Characteristics
Output Characteristics

Drain-Source On Resistance

Drain-Source On Resistance

Source-Drain Diode Forward

Capacitance

Gate Charge


Typical Characteristics
P-Ch MOSFET
